

Department of Electronics and Communication Engineering
National Institute of Technology Silchar

Programme Outcomes (POs) of M. Tech in Microelectronics and VLSI Design are as follows-

- PO1: An ability to independently carry out research/investigation and development work to solve practical problems.
- PO2: An ability to write and present a substantial technical report/document.
- PO3: Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.
- PO4: An ability to understand and design advanced VLSI based system and conduct experiments, analyze and interpret results.
- PO5: To use the techniques, skills, modern electronic design automation tools, software and equipment necessary to evaluate and analyze the systems in VLSI design environments.
- PO6: Graduates will be able to plan, conduct an organized and systematic study on significant research topic within the field of VLSI and its allied field.